

**NONPROVISIONAL PATENT
APPLICATION TRANSMITTAL RULE §1.53(b)
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Customer No. 004372
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Docket No. 108066-00092

Date: July 29, 2003

Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is a nonprovisional patent application:

For (Title): SEMICONDUCTOR INTEGRATED CIRCUIT HAVING CONTROLLABLE
INTERNAL SUPPLY VOLTAGE

By (Inventors): Hiroyuki ABE, Yutaka TAKASUKA, Hisao ISE, Hiroko TAKANO, Sachie
TAKAHASHI

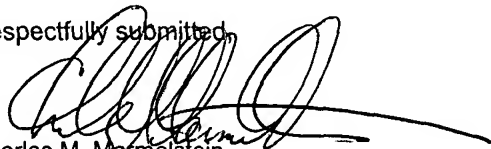
- ☒ 35 pages of Specification/Claims 1-15/Abstract are attached.
- ☒ Formal drawings (Fig(s). 1 - 5; 4 sheet(s)) is/are attached.
- ☒ An Information Disclosure Statement is attached, along with Form PTO-1449, and 3 reference(s).
- ☒ Priority of foreign application No. 2002-231451 filed August 8, 2002 in Japan is claimed under 35 U.S.C. §119.
- ☒ A certified copy of the above corresponding foreign application is attached.

The filing fee is calculated below and includes claim status after entry of any Preliminary Amendment noted above:

			SMALL ENTITY		OR	LARGE ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE		RATE	FEE
BASIC FEE				\$ 375	OR		\$ 750
TOTAL CLAIMS	15 - 20	= 0	x 9 =	\$	OR	x 18	\$
INDEP CLAIMS	1 - 3	= 0	x 42 =	\$	OR	x 84	\$
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+140 =	\$	OR	+280	\$
			TOTAL	\$	OR	TOTAL	\$ 750

- ☒ A check for the filing fee is not enclosed at this time.

Respectfully submitted,


Charles M. Marmelstein
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CMM/cam

